



PATENT
Customer No. 22,852
Attorney Docket No. 04329.2270-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Shinsuke SAKAMOTO et al.) Group Art Unit: 2815
Application No.: 10/081,109) Examiner: Eugene Lee
Filed: February 25, 2002)
For: SEMICONDUCTOR)
INTEGRATED CIRCUIT DEVICE)
AND WIRING ARRANGING)
METHOD THEREOF)

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

REQUEST FOR APPROVAL OF DRAWING CHANGE

Subject to the approval of the Examiner, it is respectfully requested that Fig. 3 in the above-captioned application be amended by including the legend "Prior Art", and Fig. 2 be amended to show four one-dotted broken lines connecting via 15 and via 35 to represent the existence of via 15 above via 35, four one-dotted broken lines connecting via 18 and via 25 to represent the existence of via 18 above via 25, and to correct lines of vias 15, 35, 18, 25 and 23 from solid to dashed or vice versa to properly represent portions that are visible and invisible. No new matter has been added. The changes are indicated in red on the attached copies of the originally filed drawings.

Upon approval of the proposed changes, Applicants respectfully request that the submission of revised drawing be deferred until after a notice of allowance has issued.

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Respectfully submitted,

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Dated: February 19, 2003

By: Tara Bleach
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